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Application No.	Filing Date	Examiner	Customer No.	1	
10/695,748	10/30/2003	M. Luhrs	30743	2824	6189
Title: STRUCTURE AND METHOD TO ENHANCE BOTH NFET AND PFET PERFORMANCE USING DIFFERENT KINDS OF STRESSED LAYERS					
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Doris et al.

Serial No.:

10/695,748

Group Art Unit:

2824

Filed:

October 30, 2003

Examiner:

M. Luhrs

For:

STRUCTURE AND METHOD TO ENHANCE BOTH NFET AND PFET

PERFORMANCE USING DIFFERENT KINDS OF STRESSED LAYERS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **STATUS INQUIRY**

Sir:

The above-identified patent application was filed on October 30, 2003. As of this date, a first action on the merits has not been received by this office and it is respectfully requested that the undersigned be provided with a statement of the status of this application.

Respectfully submitted,

Marshall M. Curtis

Reg. No. 33,138

Whitham, Curtis & Christofferson, P.C. 11491 Sunset Hills Road, Suite 340 Reston, VA 20190 (703) 787-9400

Customer Number 30743